A. MAXIMUM RATING:

1. Input Power Level: 15 dBm
2. DC Voltage : 6V
3. Operating Temperature: -30°C to +85°C
4. Storage Temperature: -40°C to +85°C

RoHS Compliant
Lead free
Lead-free soldering

Electrostatic **S**ensitive **D**evice

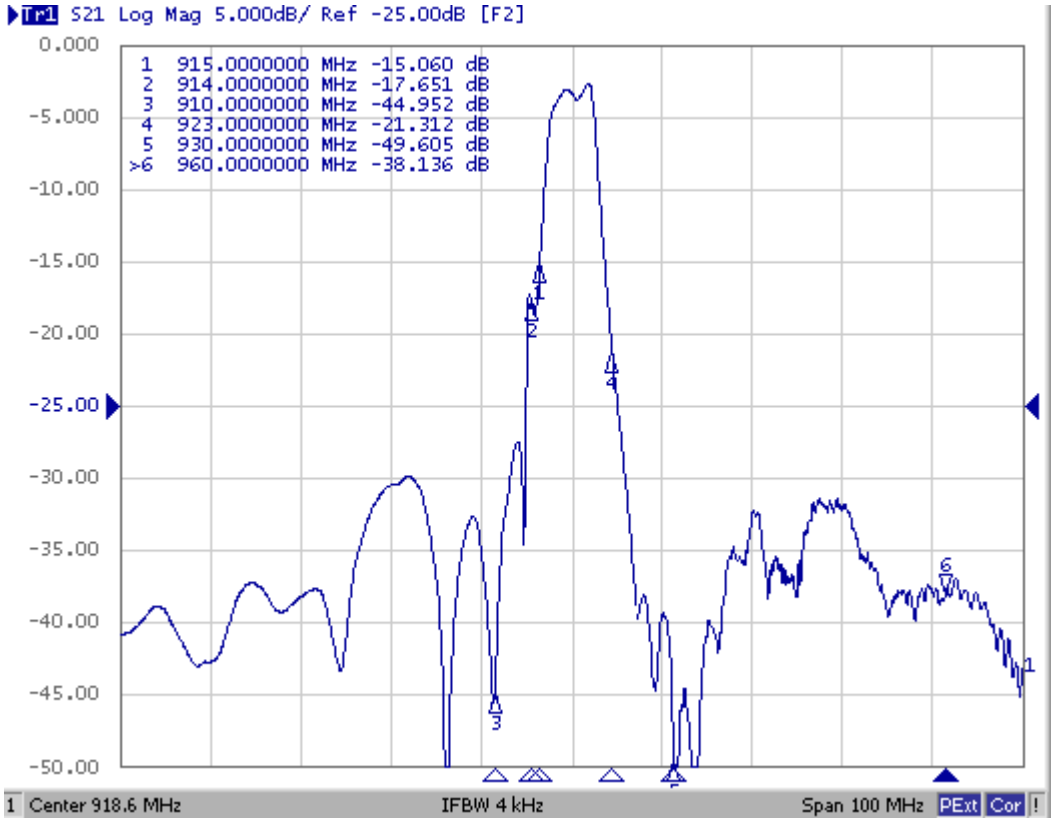
B. ELECTRICAL CHARACTERISTICS:

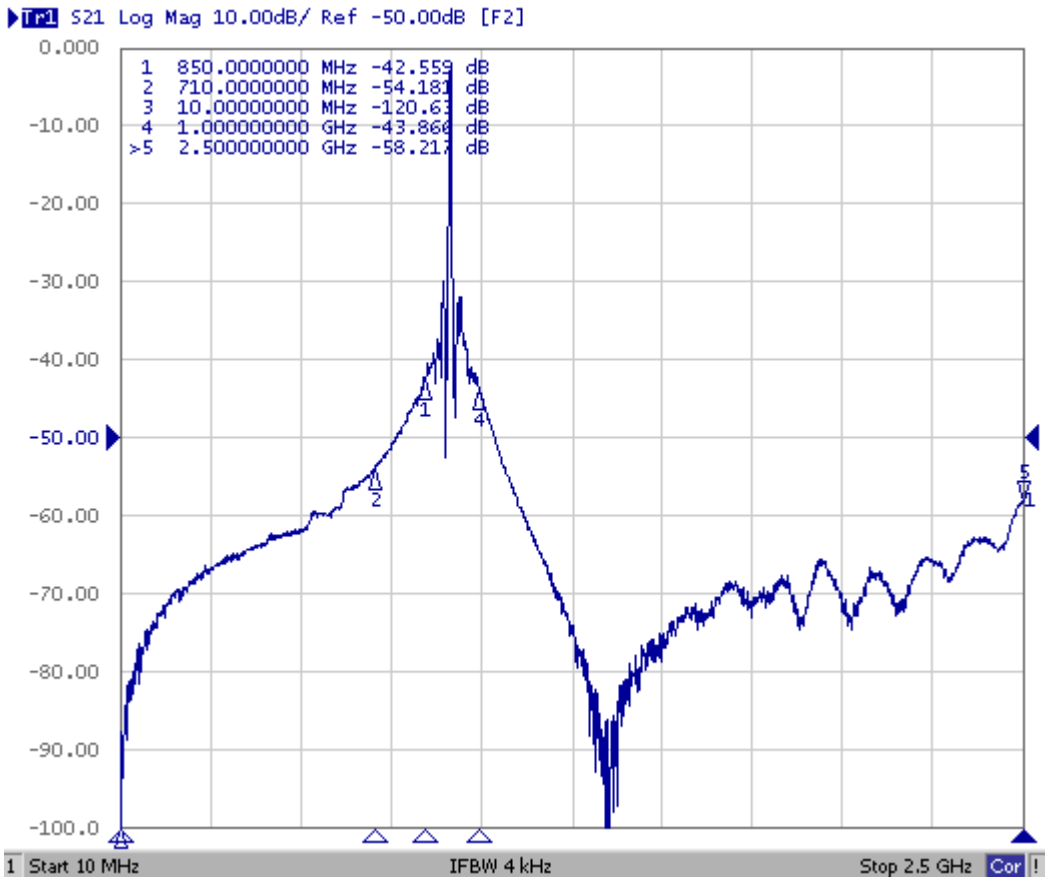
Item			Unit	Min.	Typ.	Max.
Center frequency	**Fc**		MHz	-	918.6	-
3dB BW			MHz	-	5.0	-
Minimum insertion loss	**IL (min)**					
Incl. loss of matching elements (Q=82) *1)			dB	-	2.7	4.2
Exclude loss in matching elements *2)			dB	-	2.5	4.0
Passband (relative to IL_{min}) *1)						
916.70 ~ 920.50 MHz			dB	-	1.2	3.0
Attenuation (relative to IL_{min}) *1)						
10.000 ~ 710.00		MHz	dB	45	52	-
710.00 ~ 850.00		MHz	dB	35	40	-
850.00 ~ 910.00		MHz	dB	20	27	-
910.00 ~ 914.00		MHz	dB	10	15	-
914.00 ~ 915.00		MHz	dB	5	12	-
923.00 ~ 930.00		MHz	dB	5	19	-
930.00 ~ 960.00		MHz	dB	17	29	-
960.00 ~ 1000.0		MHz	dB	28	35	-
1000.0 ~ 2500.0		MHz	dB	30	41	-
Turnover To			Deg.C	-	70	-
Temperature coefficient (TCf)			ppm/c*2	-	0.047	-
Impedance at Fc, Input	**Zin = Rin//Cin**	Zs	Ω		62Ω//1.35pF	
Impedance at Fc, Output	**Zout = Rout//Cout**	ZL	Ω		58Ω//1.44pF	

*1)： The matching circuit is real by actual passive components.
0805 Coillcraft CS series chip conductor is used for inductor.
0402 muRata GRM series is used for capacitor.

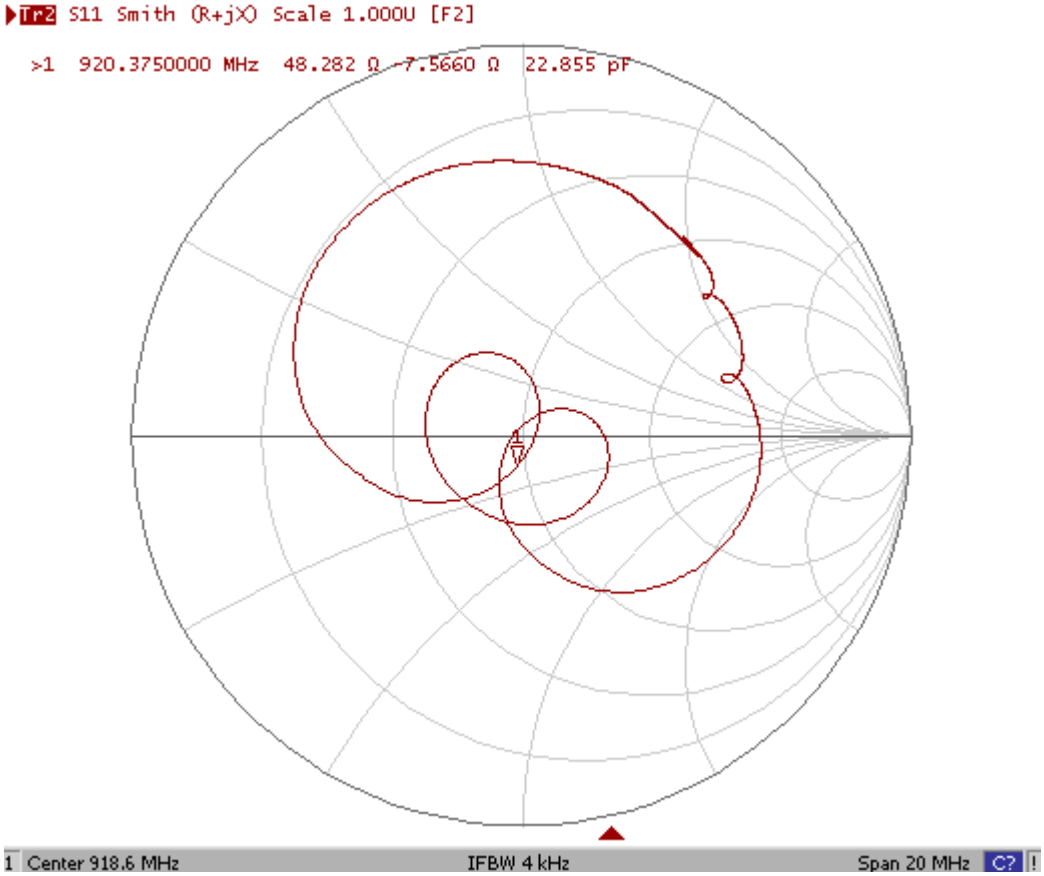
*2)： The matching circuit is ideal by simulation.

C. Frequency Characteristics :

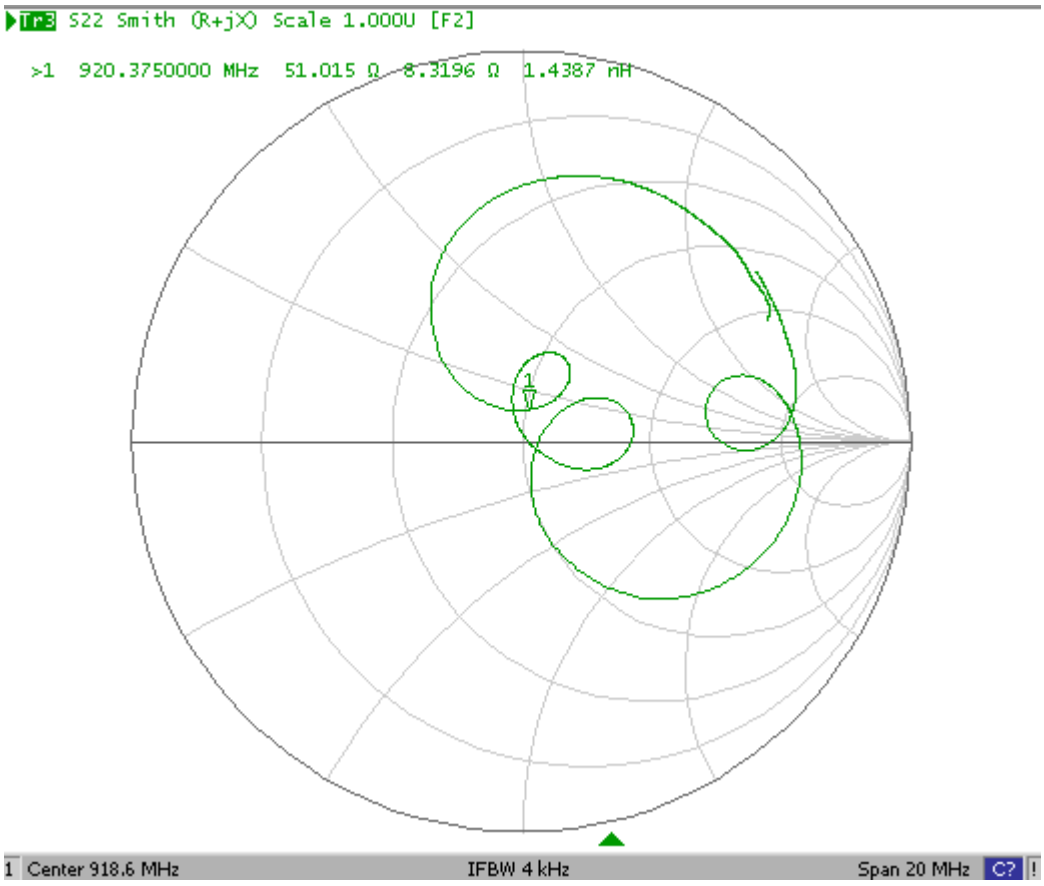




Smith Chart S11：

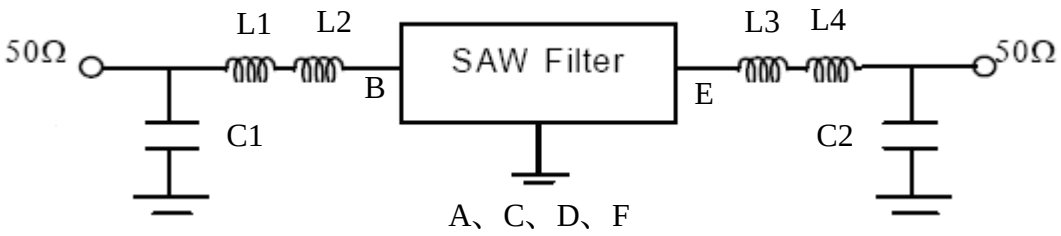


Smith Chart S22：



D. MEASUREMENT CIRCUIT:

The matching circuit is real by actual passive components.

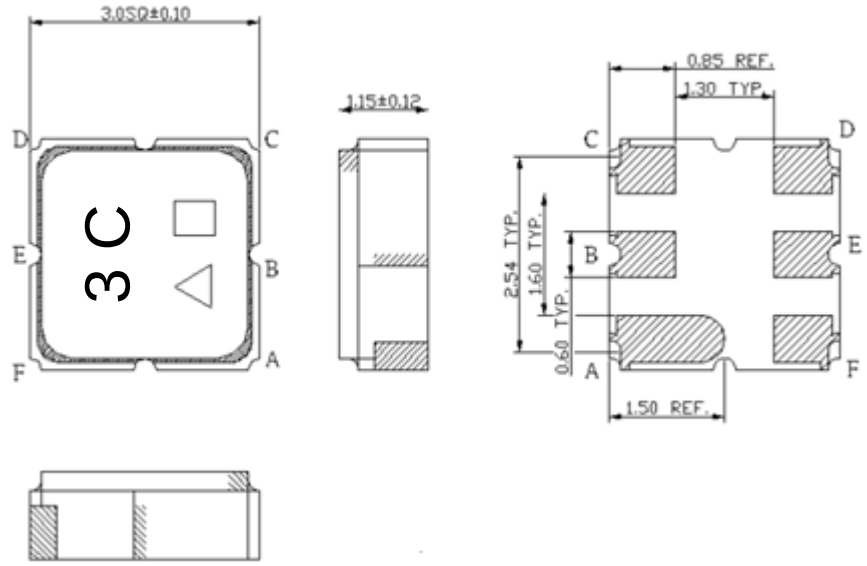


L1：10nH , L2：8.2nH , L3：8.2nH , L4：10nH

L1+L2=18.2nH ；L3+L4=18.2nH

C1：4.7 pF , C2：4.0pF

E.OUTLINE DRAWING:



A：Input ground (recommended) or Input

B：Input (recommended) or Input ground

D：Output ground (recommended) or Output

E:：Output (recommended) or Output ground

C、F:　Case Ground

Δ：Year Code

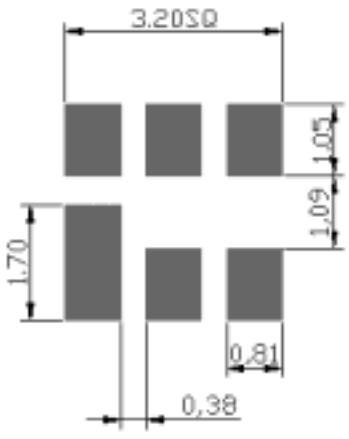
□：Data Code(Follow the table provided by planer each year)

Unit：mm

WK01	WK02	WK03	WK04	WK05	WK06	WK07	WK08	WK09	WK10	WK11	WK12	WK13
A	B	C	D	E	F	G	H	I	J	K	L	M
WK14	WK15	WK16	WK17	WK18	WK19	WK20	WK21	WK22	WK23	WK24	WK25	WK26
N	O	P	Q	R	S	T	U	V	W	X	Y	Z
WK27	WK28	WK29	WK30	WK31	WK32	WK33	WK34	WK35	WK36	WK37	WK38	WK39
a	b	c	d	e	f	g	h	i	j	k	l	m
WK40	WK41	WK42	WK43	WK44	WK45	WK46	WK47	WK48	WK49	WK50	WK51	WK52
n	o	p	q	r	s	t	u	v	w	x	y	z

*3) The recommended pin configuration offers better suppression of electrical crosstalk.

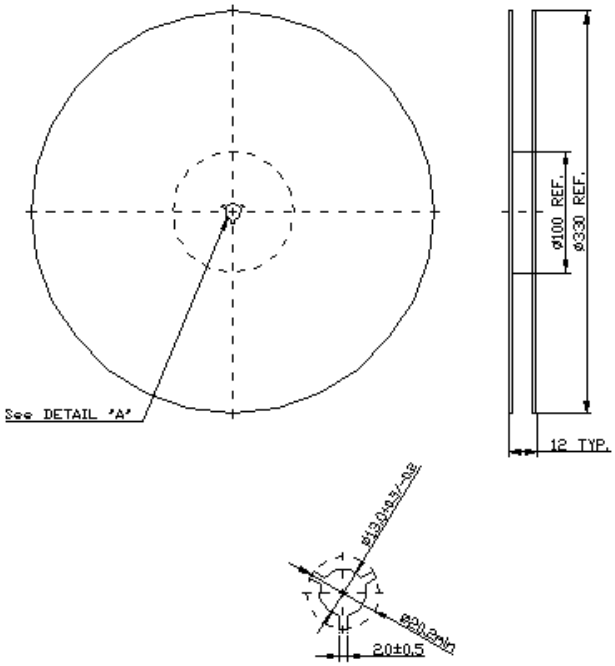
F. PCB Footprint:



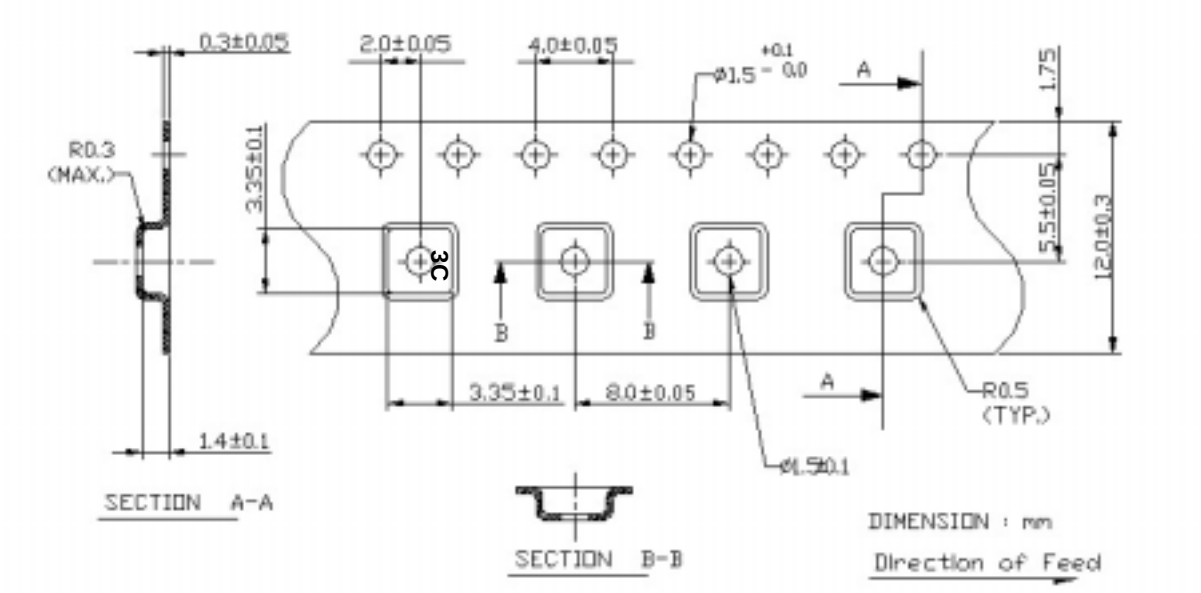
G. PACKING:

1. REEL DIMENSION

(Please refer to FR-75D10 for packing quantity)



2. TAPE DIMENSION



H. <u>RECOMMENDED REFLOW PROFILE</u> :